

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Marc Tremblay and William Joy

Assignee: Sun Microsystems, Inc.

Title: LOCAL AND GLOBAL REGISTER PARTITIONING IN A VLIW PROCESSOR

Serial No.: Unknown 09/204,585 Filed:

Herewith

Examiner: Unknown

Group Art Unit: Unknown

Docket No.: SP-3288 US



San Jose, California
December 3, 1998

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) brings the following documents to the Examiner's attention in the above-captioned application.

A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

Respectfully submitted,

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